

FIG.1A

NARROW SILICON

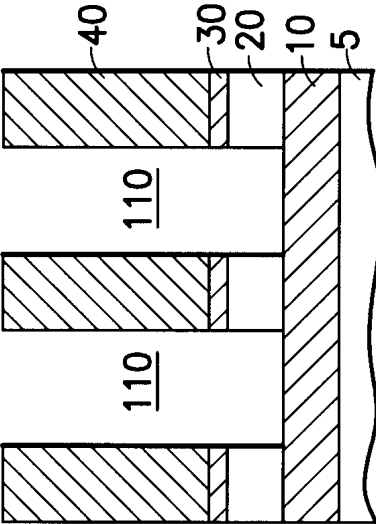


FIG.1B

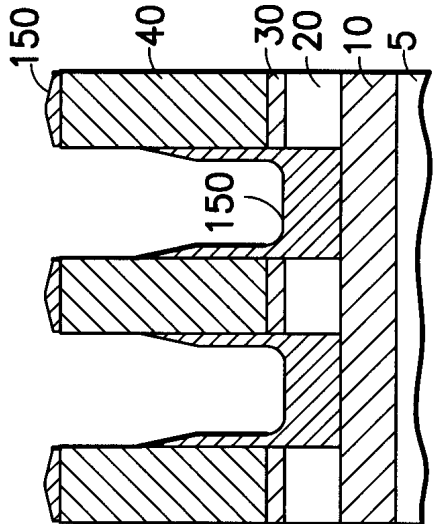


FIG.1C

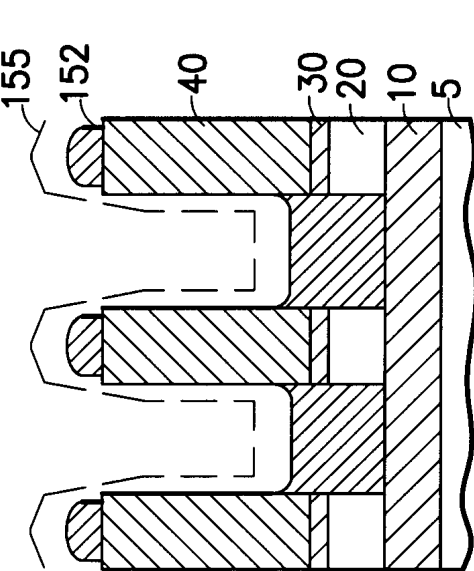


FIG.1D

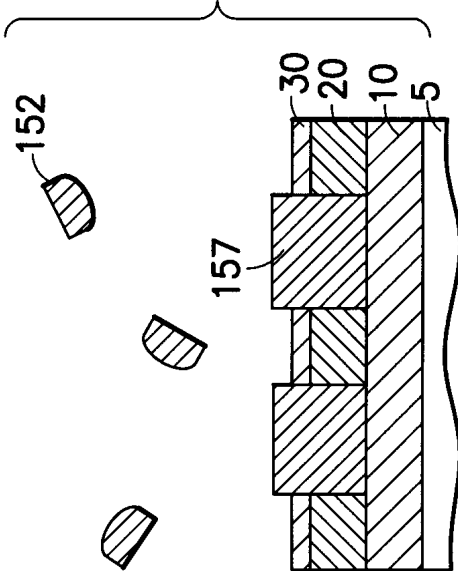


FIG.1E

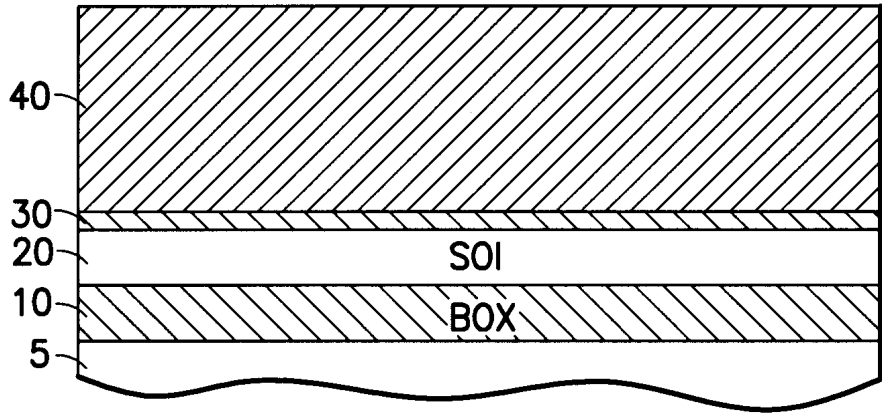


FIG.2A

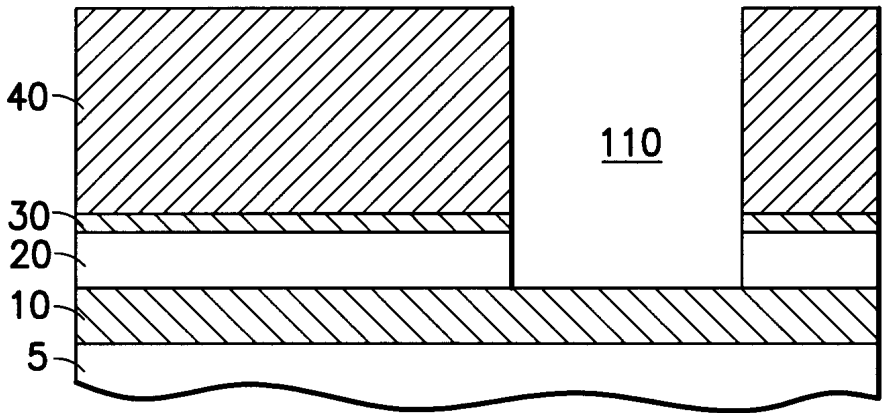


FIG.2B

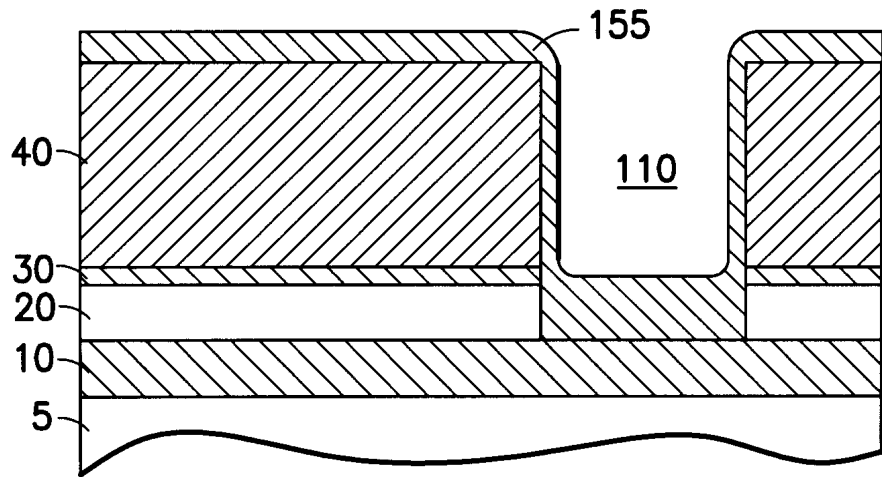
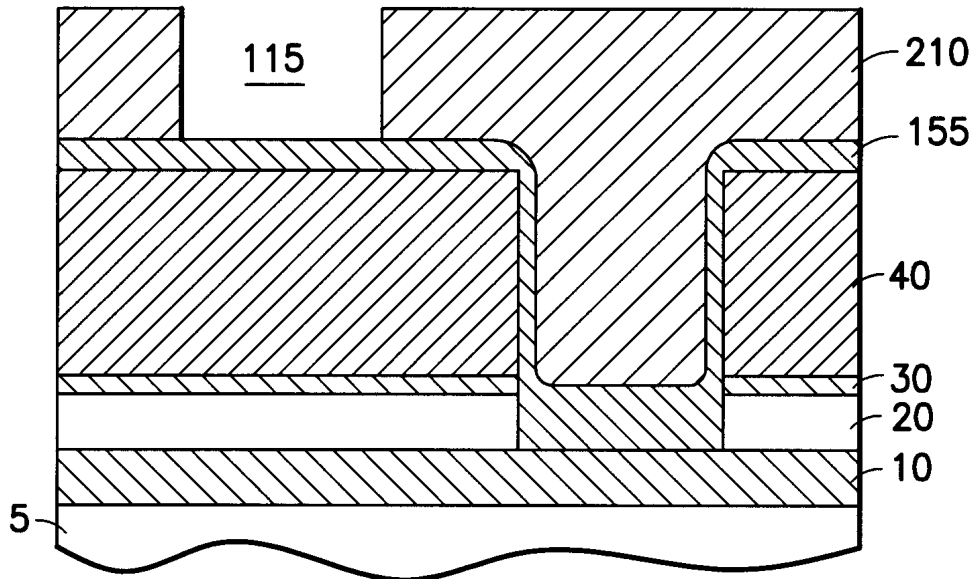
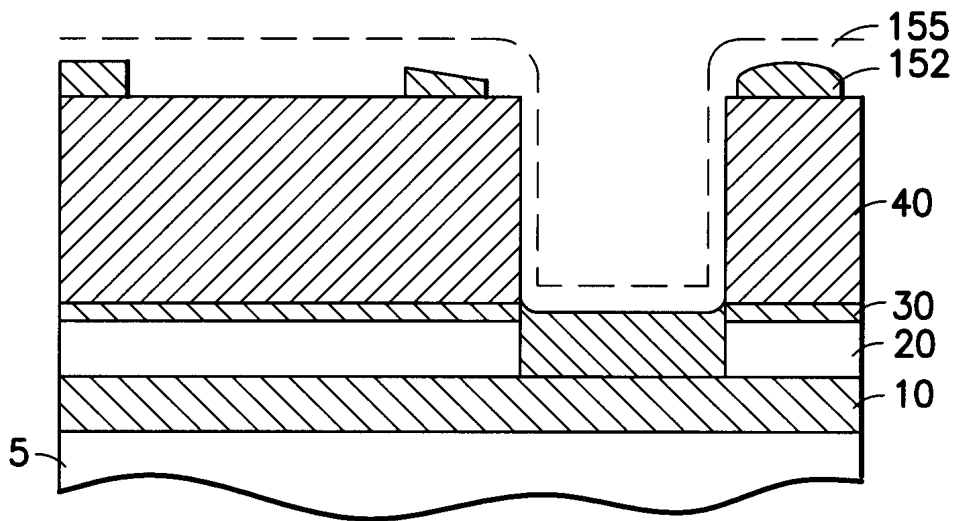


FIG.2C



**FIG.2D**



**FIG.2E**

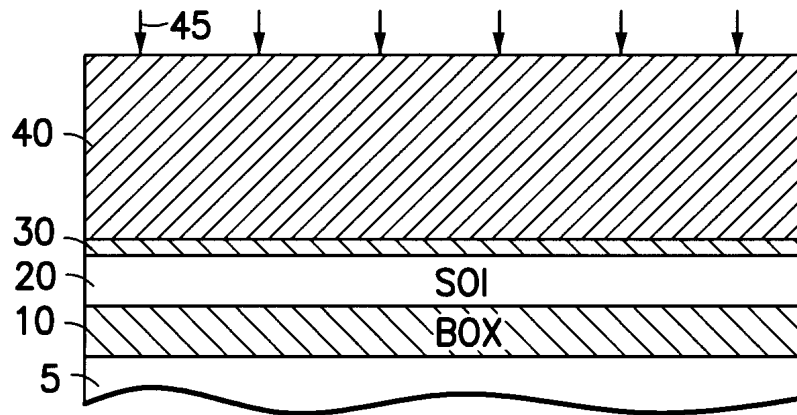


FIG. 3A

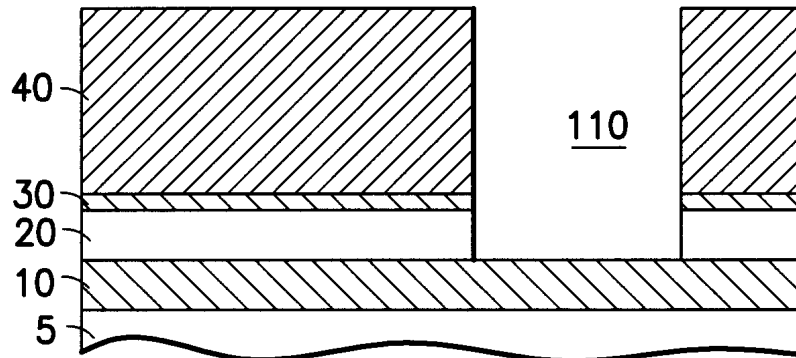


FIG. 3B

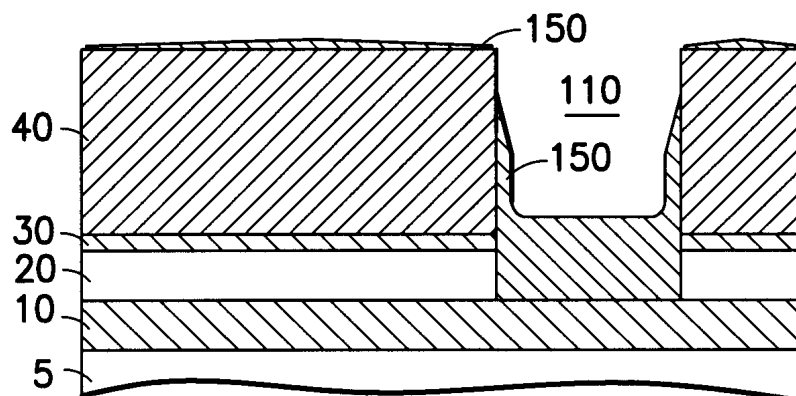


FIG. 3C

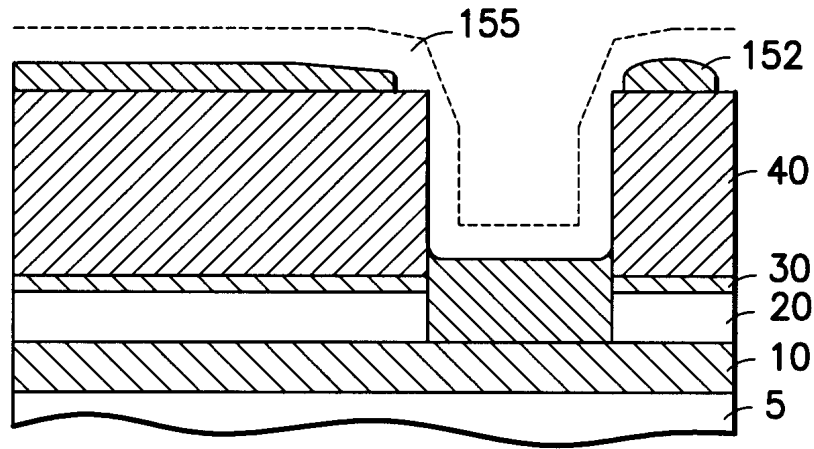


FIG.3D

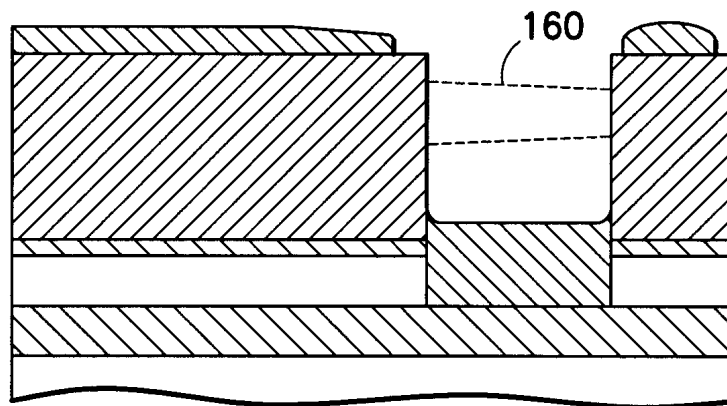


FIG.3E

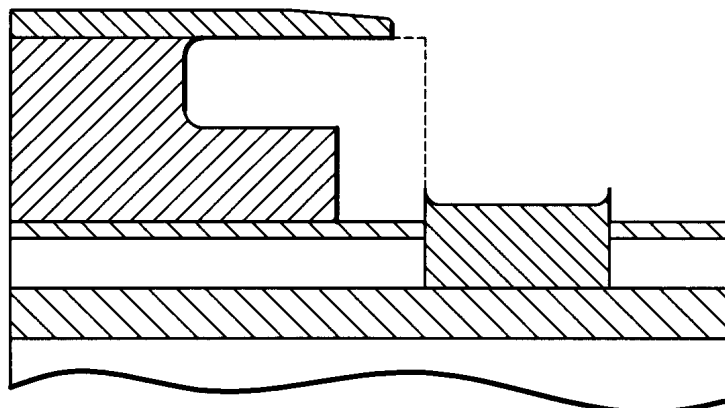


FIG.3F